

EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
S49	12	717/135.cccls. (simulat\$4 near3 (numeric\$4 or comput\$8)) (fpga or dsp)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2007/03/13 13:50
S48	68	717/135.cccls. (simulat\$4 near3 (numeric\$4 or comput\$8))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2007/03/13 13:50
S47	7	fft vhdl fpga (simulat\$6 near6 (numeric\$4 or computation\$4 or comput\$4)) (dsp) simulat\$4.clm. (parallel or shar\$4 or path)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2007/03/13 11:43
S45	18	fft vhdl fpga (simulat\$6 near6 (numeric\$4 or computation\$4 or comput\$4)) (dsp)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2007/03/13 11:39
S44	20	fft vhdl fpga (simulat\$6 near6 (numeric\$4 or computation\$4 or comput\$4)) (fpga or dsp)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2007/03/13 11:37
S43	20	fft vhdl fpga (simulat\$6 near6 (numeric\$4 or computation\$4 or comput\$4))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2007/03/13 11:37
S42	18	fft vhdl fpga (simulation near9 (numeric\$4 or computation\$4 or comput\$4))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2007/03/13 11:36
S39	55	fft vhdl fpga simulation	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2007/03/13 11:35
S22	9	((program\$6 near8 (fpga or dsp)) same vhdl) ((numer\$4 or computation\$2) near6 simulat\$6) ((simulat\$4 or comput\$8) with (fpga or dsp)) ((simulat\$4 or comput\$8) with (processor or (process\$4 near2 unit) or cpu)) path	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2007/03/13 11:33
S7	28	vhdl fpga (numer\$4 near4 simulat\$6) (processor or (process\$4 near2 unit) or cpu).((data or input or output) near6 (transmit\$4 or transfer\$4 or exchang\$4 or path))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2007/03/13 08:29
S30	8	((program\$6 near8 (fpga or dsp)) same vhdl) ((numer\$4 or computation\$2 or computing) near4 simulat\$6) ((simulat\$4 or comput\$8) with (fpga or dsp)with (data or input)) ((simulat\$4 or comput\$8) with (processor or (process\$4 near2 unit) or cpu) with (data or input) path	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2007/03/12 15:52

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S25	9	((program\$6 near8 (fpga or dsp)) same vhdl) ((numeri\$4 or computation\$2 or computing) near4 simulat\$6) ((simulat\$4 or comput\$8) with (fpga or dsp)) ((simulat\$4 or comput\$8) with (processor or (process\$4 near2 unit) or cpu)) path	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2007/03/12 15:51
S24	5	((program\$6 near8 (fpga or dsp)) same vhdl) ((numeri\$4 or computation\$2 or computing) adj4 simulat\$6) ((simulat\$4 or comput\$8) with (fpga or dsp)) ((simulat\$4 or comput\$8) with (processor or (process\$4 near2 unit) or cpu)) path	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2007/03/12 15:30
S21	15	((program\$6 near8 (fpga or dsp)) same vhdl) ((numeri\$4 or computation\$2) near6 simulat\$6) ((simulat\$4 or comput\$8) with (fpga or dsp)) ((simulat\$4 or comput\$8) with (processor or (process\$4 near2 unit) or cpu))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2007/03/12 15:00
S20	13	((program\$4 near8 (fpga or dsp)) same vhdl) ((numeri\$4 or computation\$2) near6 simulat\$6) ((simulat\$4 or comput\$8) with (fpga or dsp)) ((simulat\$4 or comput\$8) with (processor or (process\$4 near2 unit) or cpu))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2007/03/12 14:38
S19	38	(program\$4 near8 (fpga or dsp)) vhdl ((numeri\$4 or computation\$2) near6 simulat\$6) (processor or (process\$4 near2 unit) or cpu) ((simulat\$4 or comput\$8) with (fpga or dsp)) ((simulat\$4 or comput\$8) with (processor or (process\$4 near2 unit) or cpu))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2007/03/12 14:35
S18	39	(program\$4 near8 (fpga or dsp)) vhdl ((numeri\$4 or computation\$2) near6 simulat\$6) (processor or (process\$4 near2 unit) or cpu) ((data or input or output) near6 (transmit\$4 or transfer\$4 or exchang\$4 or path))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2007/03/12 14:29
S11	33	vhdl fpga (numeri\$4 near8 simulat\$6) (processor or (process\$4 near2 unit) or cpu) ((data or input or output) near6 (transmit\$4 or transfer\$4 or exchang\$4 or path))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2007/03/12 14:25
S15	41	(fpga or dsp) vhdl ((numeri\$4 or computation\$4) near8 simulat\$6) (processor or (process\$4 near2 unit) or cpu) (data near6 (transmit\$4 or transfer\$4 or exchang\$4 or path)) (simulat\$4.clm. or simulat\$4.ti.)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2007/03/12 13:14
S16	15	(fpga dsp) vhdl ((numeri\$4 or computation\$4) near8 simulat\$6) (processor or (process\$4 near2 unit) or cpu) (data near6 (transmit\$4 or transfer\$4 or exchang\$4 or path)) (simulat\$4.clm. or simulat\$4.ti.)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2007/03/12 13:13
S14	73	(fpga or dsp) vhdl ((numeri\$4 or computation\$4) near8 simulat\$6) (processor or (process\$4 near2 unit) or cpu) (data near6 (transmit\$4 or transfer\$4 or exchang\$4 or path))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2007/03/12 13:09
S13	33	(fpga or dsp) vhdl (numeri\$4 near8 simulat\$6) (processor or (process\$4 near2 unit) or cpu) (data near6 (transmit\$4 or transfer\$4 or exchang\$4 or path))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2007/03/12 13:08

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S10	31	vhdl fpga (numeri\$4 near8 simulat\$6) (processor or (process\$4 near2 unit) or cpu) (data near6 (transmit\$4 or transfer\$4 or exchang\$4 or path))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2007/03/12 13:07
S9	36	vhdl fpga (numeri\$4 near8 simulat\$6) (processor or (process\$4 near2 unit) or cpu)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2007/03/12 08:51
S5	31	vhdl fpga (numeri\$4 near4 simulat\$6) (processor or (process\$4 near2 unit) or cpu)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2007/03/12 08:50
S8	32	vhdl fpga (numeri\$4 near4 simulat\$6)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2007/03/12 08:49
S2	32	vhdl fpga (numeri\$4 near4 simulat\$6)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2007/03/12 08:49
S6	26	vhdl fpga (numeri\$4 near4 simulat\$6) (processor or (process\$4 near2 unit) or cpu) (data near6 (transmit\$4 or transfer\$4 or exchang\$4 or path))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2007/03/03 16:43
S4	31	vhdl fpga (numeri\$4 near4 simulat\$6) (processor or (process\$4 near2 unit))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2007/03/03 16:41
S3	22	vhdl fpga (numeri\$4 near4 simulat\$6) processor	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2007/03/03 16:41
S1	1660	vhdl fpga	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2007/03/03 16:36